

Scheme of Examination for M.Tech E&TC (VLSI & Embedded System) Course												
Branch- E&TC												
Semester- I												
Subject code	Subject Name	Teaching scheme (Weekly Load in Hrs)				Credits	Evaluation Scheme					ESE Duration (Hrs)
		Lecture	Tutorial	Practical	Total		Theory			Practical (Cont. Ass.)	Total	
							TAE 20%	CAE 20%	ESE 60%			
MVEL501	Digital CMOS Design	3	1	–	4	4	20	20	60	–	100	3
MVEL502	Embedded System Design	3	1	–	4	4	20	20	60	–	100	3
MVEL503	Reconfigurable Computing	3	1	–	4	4	20	20	60	–	100	3
MVEL504	Research Methodology	3	–	–	3	3	20	20	60	–	100	3
MVEL505	Elective I	3	–	–	3	3	20	20	60	–	100	3
MVEP506	Lab I	–	–	4	4	2	–	–	–	100	100	–
MSDP501	Advanced Skill Development			2	2	AU				–		
	Total	15	3	6	24	20	100	100	300		600	
Elective-I MVEL505												
MVEL505A. Real Time Operating System												
MVEL505B. Mixed Signal Circuit Design												
MVEL505C. CMOS RF Circuit Design												
MVEL505D. Wireless sensor Network												

Scheme of Examination for M.Tech E&TC (VLSI & Embedded System) Course												
Branch- E&TC												
Semester- II												
Subject code	Subject Name	Teaching scheme (Weekly Load in hrs)				Credits	Evaluation Scheme					ESE Duration (Hrs)
		Lecture	Tutorial	Practical	Total		Theory			Practical (Con.Ass.+ External)	Total	
							TAE 20%	CAE 20%	ESE 60%			
MVEL508	Analog CMOS Design	3	1	–	4	4	20	20	60	–	100	3
MVEL509	System on Chip	3	1	–	4	4	20	20	60	–	100	3
MVEL510	Processor Design	3		–	3	3	20	20	60	–	100	3
MVEL511	Elective II	3	–	–	3	3	20	20	60	–	100	3
MVEL512	Elective III	3			3	3	20	20	60		100	
MVEP513	Lab Practice II	–	–	4	4	2	–	–	–	100	100	–
MVEP514	Seminar I			2	2	2				100	100	
Total		15	2	6	21	21	100	100	300	200	700	–

Elective-III (MVEL512)	Elective - II MVEL511
MVEL512A. ASIC Design	MVEL511A. Microelectronic microwave switches Design(MEMS)
MVEL512B. Network on Chip	
MVEL512C. DSP Processor & Architectures	MVEL511B. VLSI Interconnection
MVEL512D. Software Defined Radio	MVEL511C. Embedded Product Design
MVEL512E. Energy Studies	MVEL511D. Optimization Technique
MVEL512F. Environmental Studies	MVEL511E. Design and Analysis of Algorithm
MVEL512G. Open Elective	MVEL511F. Industry Offered Elective

Scheme of Examination for M. Tech. E & TC Engineering													
VLSI Embedded System Course													
Semester- III													
Subject code	Subject Name	Teaching scheme (Weekly Load in hrs.)				Credits	Evaluation Scheme						ESE Duration (Hrs.)
		Lecture	Tutorial	Practical	Total		Theory			PR		Total	
							TAE 20 %	CAE 20 %	ESE 60%	Conti. Ass.	External		
MVEP601	Technical Writing (LATEX)	-	-	2	2	2	-	-	-	50	50	100	-
MVEP602	Seminar-II	-	-	4	4	4	-	-	-	50	50	100	-
MVEP603	Dissertation Phase I	-	-	8	8	8	-	-	-	150	50	200	-
	Total	-	-	14	14	14	-	-	-	250	150	400	-

Scheme of Examination for M. Tech. E & TC Engineering													
VLSI Embedded System Course													
Semester-IV													
Subject code	Subject Name	Teaching scheme (Weekly Load in hrs.)				Credits	Evaluation Scheme						ESE Duration (Hrs.)
		Lecture	Tutorial	Practical	Total		Theory			PR		Total	
							TAE 20 %	CAE 20 %	ESE 60%	Conti. Ass.	External		
MVEP604	Seminar - III	-	-	4	4	4	-	-	-	50	50	100	-
MVEP605	Dissertation Phase II	-	-	16	16	16	-	-	-	150	50	200	-
	Total	-	-	20	20	20	-	-	-	200	100	300	-